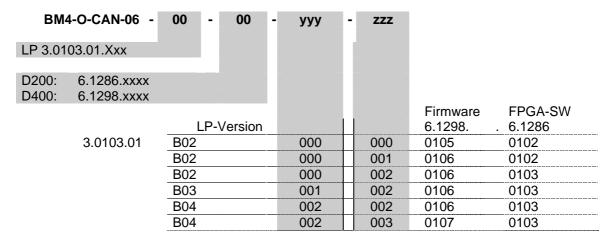# Execution CANsync-Master Interfaces

1. Standard CANsync-Master Interfaces

Typedesignation: BM4-O-CAN-06-00-00
Part No.:350446
Function: CANsync-Master Interfaces
Presuppositions: BM44XX-XX-XXXXX-XX

Information of nameplate:

BM4-O-CAN-06	-	00	-	00	-	yyy	-	zzz

LP 3.0103.01.Xxx

D200:   6.1286.xxxx
D400:   6.1298.xxxx

				Firmware 6.1298.	FPGA-SW . 6.1286
	LP-Version				
3.0103.01	B02	000	000	0105	0102
	B02	000	001	0106	0102
	B02	000	002	0106	0103
	B03	001	002	0106	0103
	B04	002	002	0106	0103
	B04	002	003	0107	0103

Hardware history
The printed circuit board 3.0103.01.B02 is the first official version.
The printed circuit board 3.0103.01.B03 is introduced with AT076_05 from March 23, 2005.
  • Optimizes boot behavior (Reset level at the option modules)
The printed circuit board 3.0103.04.B04 is introduced with AT141_07 from May 05, 2007.
  • Optimizing of Assembly (No Boot Strip Loader Function)

FPGA software history
The FPGA-software 1.02 is the first official version.
The FPGA-software 1.03 is introduced with AT076_05 from April 05, 2005.
  • Synchronization behavior improves with restart of the synchronization
  • Improvement of the BACI-Timing

Firmware history
The Firmware-software 1.05 is the first official version.
The Firmware-software 1.06 is introduced with AT201_04 from April 14, 2004.
The following mistakes are repaired:
  • Double actual value of the slave, internal timing of the master
  • Telegrams are asynchronously to CANsync tact in cluster modus
  • Slave 0 cannot be addressed with direct address
  • Wrong value in the BACI-Header
  • Busy-Flag stops with mistake in the command's channel
The Firmware-software 1.07 is introduced with AT149_07 from May 21, 2007
  • Correction of communication mechanism for slaves with addresses higher than 15

Indication
Motion Control is possible from SW1.06

Update-Indication
Software-Update and Firmware-Update are possible with adapter. Firmware and FPGA-SW always together update at last version.